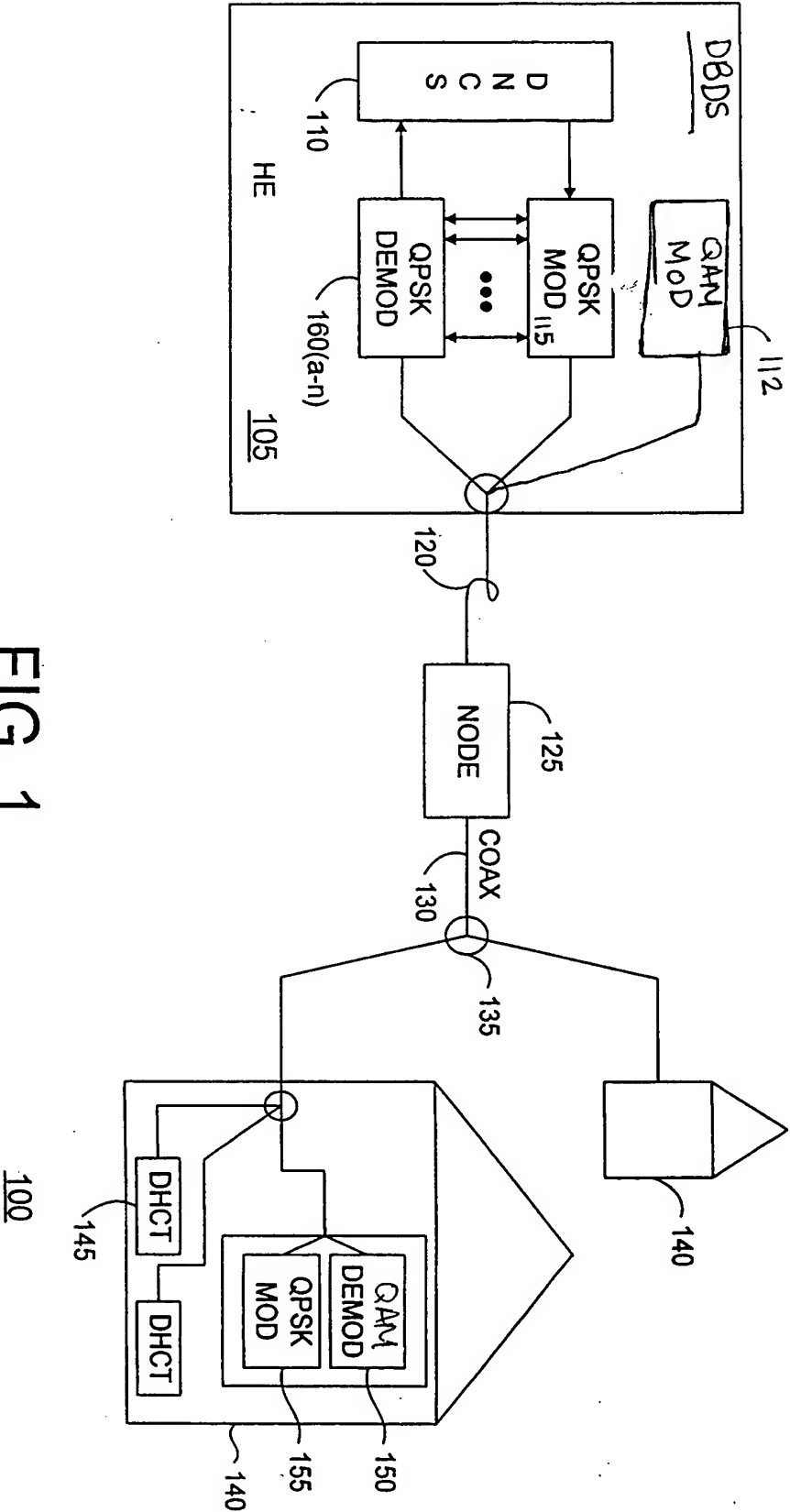


FIG. 1
Prior Art



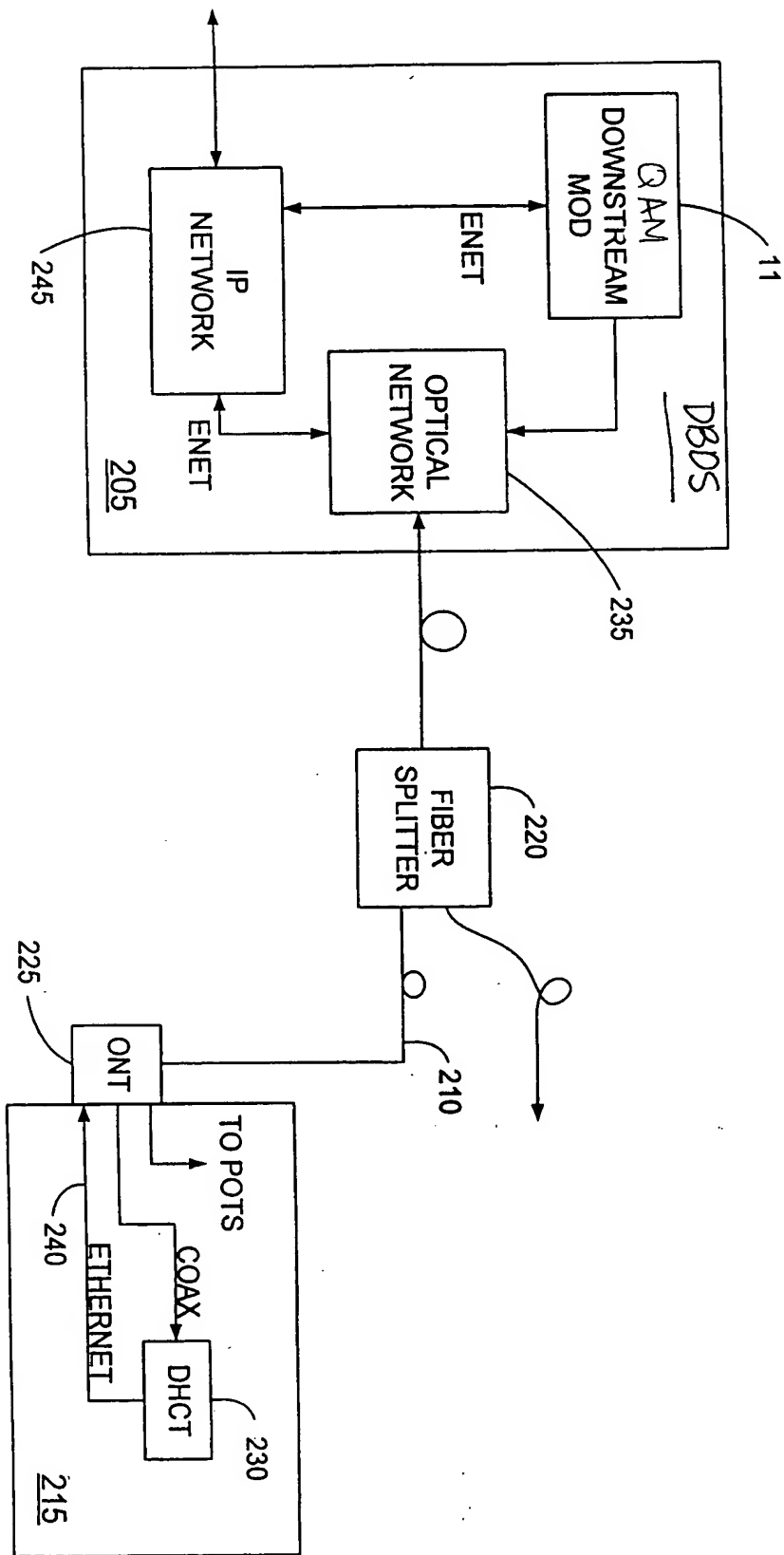


FIG. 2
Prior Art

200

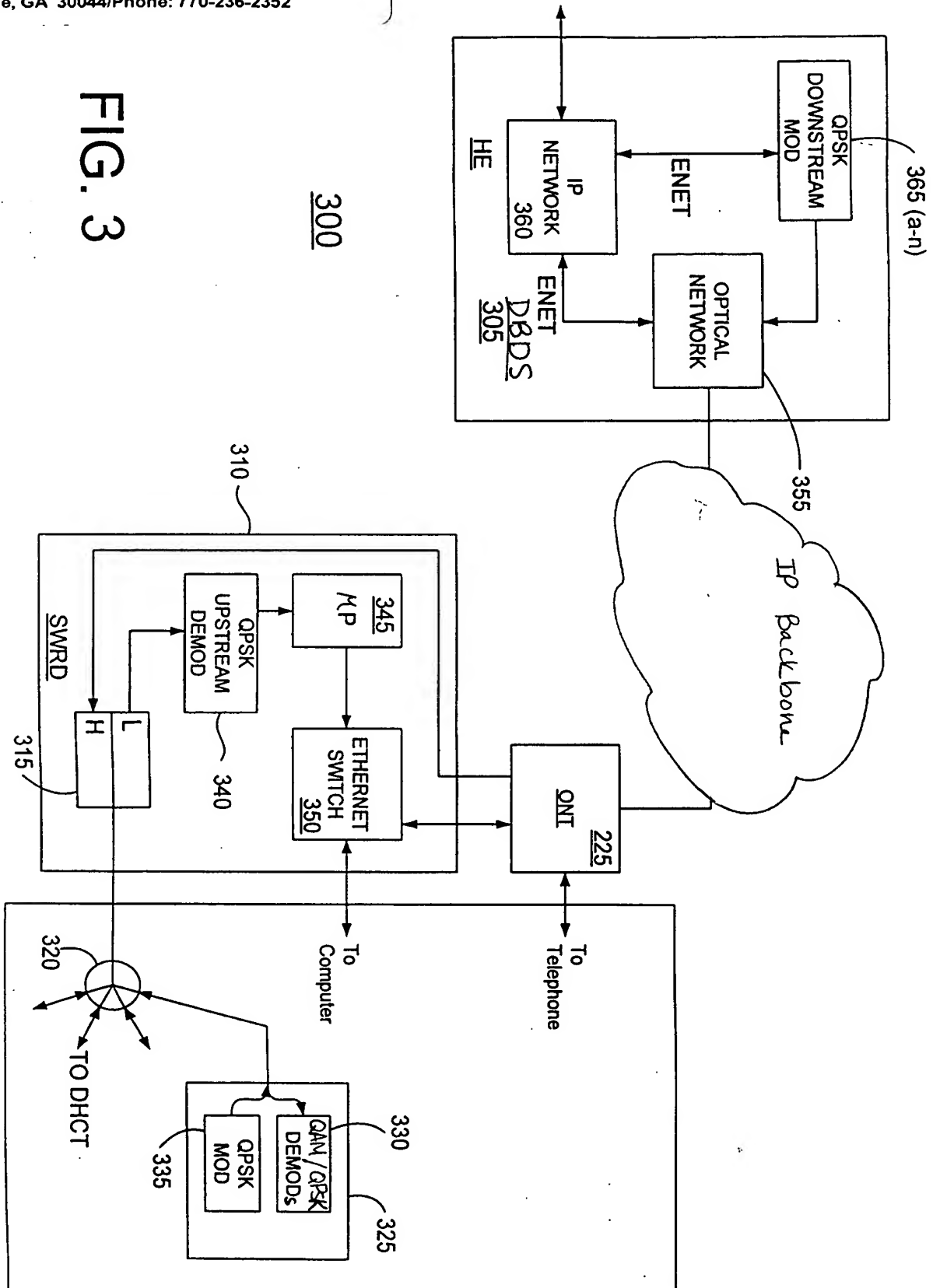


FIG. 3

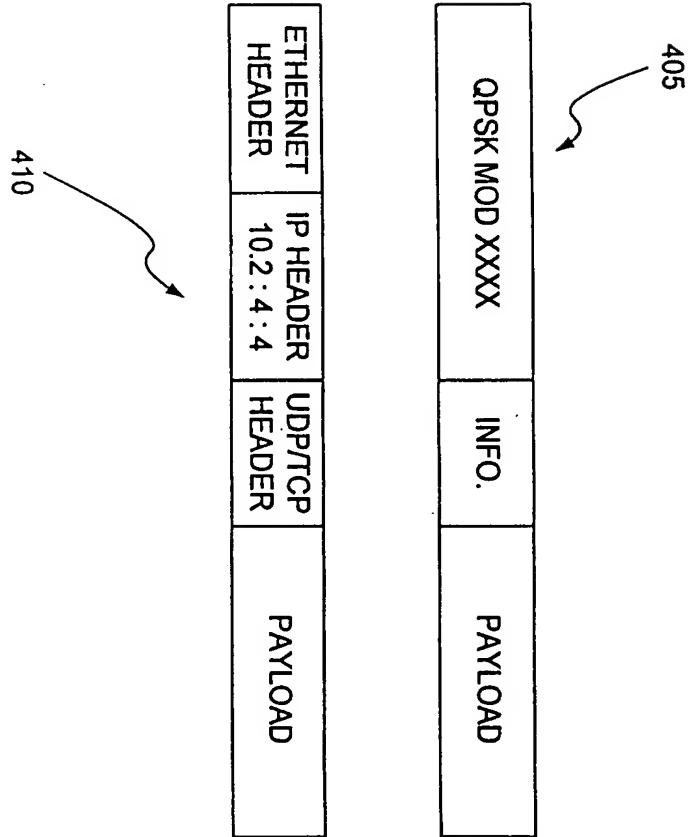


FIG. 4

The diagram illustrates a network architecture. On the left, a cloud labeled "IP Backbone" is connected to a box labeled "Headend 305". The Headend is connected to a box labeled "ONT 225". The ONT is connected to a large rectangular box labeled "310". Inside box 310, there is an "Ethernet Switch 350" connected to a "QAM Modulator 355". The QAM Modulator is connected to a "QPSK Upstream Demodulator 340". The QPSK Upstream Demodulator is connected to a "QPSK Mod 335". The QPSK Mod is connected to a "DHCT" block. The DHCT block is connected to a circular component labeled "320". The circular component has two arrows pointing away from it, both labeled "To DHCT". The entire system is enclosed in a larger box labeled "310".